

#9B/510-4  
u Jan

PATENT  
0509-1032

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Romain DESPLATS et al.

Conf. 5660

Application No. 09/831,525

Group 2133

Filed May 10, 2001

Examiner Cynthia H. Britt

METHOD AND INSTALLATION FOR FAST  
LOCATION OF A FAULT IN AN INTEGRATED  
CIRCUIT

RECEIVED

MAY 03 2004

Technology Center 2100

April 29, 2004

AMENDMENT

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Official Action mailed January 29,  
2004, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of  
this paper.

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 3 of this paper.

**Amendments to the Drawings** begin on page 20 of this  
paper.

**Remarks** begin on page 21 of this paper.

An **Appendix** is attached following the signature page  
of this paper.